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APPLICANT

Yasuyo MARUWAKA, et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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C. L. WEY; "Design of fast high-radix SRT dividers and their VLSI implementation," IEEE Proceedings E. Computers & Digital Techniques, Institution of Electrical Engineers, Stevenage, GB, vol. 147, no. 4, July 28, 2000, XP006013922, ISSN: 1350-2387, pp. 275-281.

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